

**PATENT APPLICATION**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of

Docket No: Q76956

Shinya WATANABE, et al.

Divisional of Appln. No. 10/067,320

Filed: February 7, 2002

Confirmation No. 4591

Group Art Unit: 2818

Examiner: Andy HUYNH

Appln. No.: Unassigned

Filed: August 27, 2003

Group Art Unit: Unassigned

For: SEMICONDUCTOR CHIP HAVING AN ARRAYED WAVEGUIDE GRATING AND  
METHOD OF MANUFACTURING THE SEMICONDUCTOR CHIP AND MODULE  
CONTAINING THE SEMICONDUCTOR CHIP

**PRELIMINARY AMENDMENT**

**MAIL STOP NON-FEE AMENDMENT**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

Prior to examination, please amend the above-identified application as follows on the  
accompanying pages.

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